

Notice of Allowability

Application No.

10/688,685

Examiner

Linh M. Nguyen

Applicant(s)

YI, SEUNG HYUN

Art Unit

2816

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 06/18/2004.
2. ☒ The allowed claim(s) is/are 1-20.
3. ☒ The drawings filed on 18 June 2004 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

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DETAILED ACTION

This is a reply to the Applicant's amendment submitted on 06/18/2004. According to this amendment, claims 19 and 20 are newly added; thus, claims 1-20 are currently presented in the instant application.

Allowable Subject Matter

1. Claims 1-20 are allowed.

Reasons for Allowance

2. The following is an examiner's statement of reasons for allowance:

The closest prior art fails to disclose or fairly suggest:

- A circuit for correcting a duty factor of a clock signal, in which the control signal generating part includes a) a first delay loop for delaying the synchronized clock generating reference signal for a preset time period, to provide a 180° clock generating phase signal, and b) a second delay loop for delaying the synchronized clock generating reference signal for a preset time period, to provide as a 360° clock generating phase signal, in combination with the remaining claimed limitations, as called for in claims 1 and 5; or
- A circuit for correcting a duty factor of a clock signal, in which the clock signal generating part includes a) a correction delay for delaying the 360° clock generating control signals, b) a first pulse signal generator for generating a pulse signal in response to the 360 clock generating control signal delayed at the correction delay, c) a second pulse signal generator for generating a pulse signal in response to the 180° clock generating control signals, in combination with the remaining claimed limitations, as called for in claim 10; or

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- A circuit for correcting a duty factor of a clock signal, in which the control generating part comprises a synchronized signal providing part for synchronizing a shifted clock generating reference signal to an input clock signal, a first delay loop for delaying the synchronized clock generating reference signal for a preset period of time, to provide as a first clock generating phase signal, and a second delay loop for delaying the synchronized clock generating reference signal for a preset period of time, to provide as a second clock generating phase signal, in combination with the remaining claimed limitations, as called for in claim 19; or

- A circuit for correcting a duty factor of a clock signal, in which the clock signal generating part comprises a correction delay for delaying the second clock generating control signal, a first pulse signal generator for generating a pulse signal in response to the second clock generating control signal delayed at the correction delay, and a second pulse signal generator for generating a pulse signal in response to the first clock generating control signal, in combination with the remaining claimed limitations, as called for in claim 20.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Inquiry

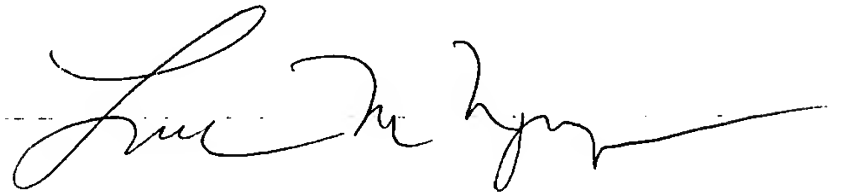
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Linh M. Nguyen whose telephone number is (571) 272-1749. The examiner can normally be reached on Alternate Mon, Tuesday - Friday from 7:00 to 4:30.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

LMN



**LINH MY NGUYEN
PRIMARY EXAMINER**